

**J C BOSE UNIVERSITY OF SCIENCE & TECHNOLOGY, YMCA, FARIDABAD**

**ELECTRONICS ENGINEERING DEPARTMENT**

**TIME TABLE MARCH, 2022**

**MTECH ECE-2 (T1)**

**Audit Course2-**

LEC	1	2	3	4	5	6	7	8	9	10
TIME		9:15-10:10	10:10-11:05	11:05-12:00	12:00-12:55	12:55-1:50	1:50-2:45	2:45-3:40	3:40-4:35	4:35-5:30
MON		ARS LAB(COMM-1 W/S)(RT)					AUD COURSE-2	AUD COURSE-2		
TUE		AIOT (RC)	ARS (RT)	ADC (NT)	ADSP (DS)		MENTOR-MENTEE PERIOD			
WED		ADSP (DS)	AIOT (RC)	AIOT (RC)	ARS (RT)		LIBRARY PERIOD			
THU		ARS (RT)	ADSP (DS)	ADC (NT)	ADC (NT)					
FRI		ADSP LAB(COMP LAB) (DS,NT)								

DS- Dushyant Shukla, NT- Neelam Turk, RT- Rohit Tripathi, RC- Rashmi Chawla

**MTECH VLSI - 2 (MPI W/S, CS LAB.)**

**Audit Course2-**

LECT	1	2	3	4	5	6	7	8	9	10
TIME		9:15-10:10	10:10-11:05	11:05-12:00	12:00-12:55	12:55-1:50	1:50-2:45	2:45-3:40	3:40-4:35	4:35-5:30
MON (MPI W/S)		ADCMOS (SJ1)	ASIC & FPGA (PK)	LPVLSI (SJ1)	VLSIDVT (NY)		AUD COURSE-2	AUD COURSE-2		
TUE (MPI W/S)		ASIC & FPGA (PK)	VLSIDVT (NY)	LPVLSI (SJ1)	ADCMOS (SJ1)		LIBRARY PERIOD			
WED (MPI W/S)		ADCMOS (NS)	VLSIDVT (NY)	ADCMOS (SJ1)	ASIC & FPGA (PK)		MENTOR-MENTEE PERIOD			
THU		VLSI DESIGN & TESTING LAB.(VLSI LAB) (NY)								
FRI (CS LAB.)		LPVLSI (SJ1)	ADCMOS (NS)	ADCMOS (NS)			ADCMOS LAB.(VLSI LAB) (NS)			

SJ1- Sunil Jadav, NS- Nitin Sachdeva, PK- Prashant Kumar, NY- Nisha Yadav

<b>ECE-6 (T2)</b>										
<b>LEC T</b>	<b>1</b>	<b>2</b>	<b>3</b>	<b>4</b>	<b>5</b>	<b>6</b>	<b>7</b>	<b>8</b>	<b>9</b>	<b>10</b>
<b>TIME</b>		<b>9:15-10:10</b>	<b>10:10-11:05</b>	<b>11:05-12:00</b>	<b>12:00-12:55</b>	<b>12:55-1:50</b>	<b>1:50-2:45</b>	<b>2:45-3:40</b>	<b>3:40-4:35</b>	<b>4:35-5:30</b>
<b>MON</b>		<b>CONTROL SYSTEM (VR)</b>	<b>DIVP (NG)</b>	<b>MCN (SK)</b>	<b>ITC (SJ)</b>	<b>LUNCH</b>	<b>DS (BB)/ CLS (MK) (EMBEDDED LAB.)</b>	<b>DIVP (NG)</b>	<b>MENTOR-MENTEE-PERIOD</b>	
<b>TUE</b>		<b>DIP LAB(G1) (COMP LAB) (MK, NG, REEMA) WORKSHOP (G2) (COMM2 W/S)</b>		<b>CN LAB(G1) (VLSI LAB) (PG, BB) WORKSHOP (G2) (COMM2 W/S)</b>		<b>LUNCH</b>	<b>EM LAB(G1) (PCI W/S) (AA2, RT) WORKSHOP (G2) (COMM2 W/S)</b>		<b>LIBRARY PERIOD</b>	
<b>WED</b>		<b>EM LAB(G2) (PCI W/S) (AA2, RT) WORKSHOP (G1) (COMM2 W/S)</b>		<b>CN LAB(G2) (VLSI LAB) (PG, BB) WORKSHOP (G1) (COMM2 W/S)</b>		<b>LUNCH</b>	<b>DIP LAB (G2) (COMP LAB) (MK, REEMA) WORKSHOP (G1) (COMM2 W/S)</b>			
<b>THU</b>		<b>CN (PG)</b>	<b>CN (PG)</b>	<b>DS (BB)/ CLS (MK) (EMBEDDED LAB.)</b>	<b>DS (BB)/ CLS (MK) (EMBEDDED LAB.)</b>	<b>LUNCH</b>	<b>CONTROL SYSTEM (VR)</b>	<b>CONTROL SYSTEM (VR)</b>		
<b>FRI</b>		<b>MCN (SK)</b>	<b>DIVP (NG)</b>	<b>ITC (SJ)</b>	<b>MCN (SK)</b>	<b>LUNCH</b>	<b>CN (PG)</b>	<b>ITC (SJ)</b>		
<b>MK- Manju Kumari, PG- Priyanka Gandhi, AA2- Archana Aggarwal, LR- Lalit Rai, SK- Sonam Khera, BB- Bharat Bhushan, NG- Neetu Gupta, SJ-Shielza Jain, VR-Vinod Rathore, RT- Rohit Tripathi</b>										

<b>EIC-6 (S8, T2, F6)</b>										
<b>LEC T</b>	<b>1</b>	<b>2</b>	<b>3</b>	<b>4</b>	<b>5</b>	<b>6</b>	<b>7</b>	<b>8</b>	<b>9</b>	<b>10</b>
<b>TIME</b>		<b>9:15-10:10</b>	<b>10:10-11:05</b>	<b>11:05-12:00</b>	<b>12:00-12:55</b>	<b>12:55-1:50</b>	<b>1:50-2:45</b>	<b>2:45-3:40</b>	<b>3:40-4:35</b>	<b>4:35-5:30</b>
<b>MON (S8)</b>		<b>IPC (LR)</b>	<b>IPC (LR)</b>	<b>SOCO (AA2)</b>	<b>SOCO (AA2)</b>	<b>LUNCH</b>	<b>CBIC (SJ)</b>	<b>MENTOR- MENTEE- PERIOD</b>		
<b>TUE (S8)</b>		<b>SOCO (AA2)</b>	<b>IPC (LR)</b>	<b>CBIC (SJ)</b>	<b>IOT (RT)</b>	<b>LUNCH</b>	<b>DSP (MK)</b>	<b>DSP (MK)</b>		
<b>WED (T2)</b>		<b>VLSI DESIGN (PK)</b>	<b>CBIC (SJ)</b>	<b>DSP LAB (G1) (COMP LAB) (MK, DS) WORKSHOP (G2) (PCI W/S)</b>		<b>LUNCH</b>	<b>VLSI DESIGN LAB(G1) (VLSI LAB) (PK, SJ1) WORKSHOP (G2) (PCI W/S)</b>		<b>WORKSHOP(G2) (PCI W/S)</b>	
<b>THU (F6)</b>		<b>VLSI DESIGN (PK)</b>	<b>VLSI DESIGN (PK)</b>	<b>IOT (RT)</b>	<b>IOT (RT)</b>	<b>LUNCH</b>	<b>DSP (MK)</b>	<b>LIBRARY PERIOD</b>		
<b>FRI</b>		<b>WORKSHOP(G1) (PCI W/S)</b>		<b>WORKSHOP (G1) (PCI W/S) VLSI DESIGN LAB(G2) (VLSI LAB) (PK, SJ1)</b>		<b>LUNCH</b>	<b>WORKSHOP (G1) (PCI W/S) DSP LAB (G2) (COMP LAB) (MK, DS)</b>			
<b>SJ- Sheilza Jain, LR- Lalit Rai, PK- Prashant Kumar, AA2- Archna Aggarwal, MK- Manju Kumari, SJ1- Sunil Jadav, DS- Dusyant Shukla, RT- Rohit Tripathi</b>										

**ENC-6 (S8)**

LECT	1	2	3	4	5	6	7	8	9	10
TIME		9:15-10:10	10:10-11:05	11:05-12:00	12:00-12:55	12:55-1:50	1:50-2:45	2:45-3:40	3:40-4:35	4:35-5:30
MON		CN (G1) (VLSI LAB.) (PG, BB, MAYANK) WORKSHOP(G2) (JUHI) (COMPUTER DEPARTMENT)		DSP LAB. (G1) (COMP LAB.) (GS, KS) WORKSHOP (G2) (JUHI) (COMPUTER DEPARTMENT)		LUNCH	MOBILE COMM. LAB. (G1) (COMM 1 W/S) (SK, LR)		MENTOR- MENTEE-PERIOD	
TUE		WORKSHOP(G1) (DIMPLE) (COMPUTER DEPARTMENT) CN (G2) (VLSI LAB.) (PG, BB, MAYANK)		WORKSHOP (G1) (DIMPLE) (COMPUTER DEPARTMENT) DSP LAB. (G2) (COMP LAB.) (GS, KS)		LUNCH	MOBILE COMM. LAB. (G2) (COMM 1 W/S) (SK, LR)		LIBRARY PERIOD	
WED		SSIC (MB)	DSP (GS)	DSP (GS)	MC (SK)	LUNCH	CN (PG)	CN (PG)	MC (SK)	
THU		SSIC (MB)	IHOT(RC)	SOEN (DNC)	DSP (GS)	LUNCH	SSIC (MB)	CN (PG)		
FRI		IHOT(RC)	IHOT(RC)	SOEN (DNC)	SOEN (DNC)	LUNCH	MESSAGE OF BG (PD)	MESSAGE OF BG (PD)	MC (SK)	
PD- Pradeep Dimri, PG- Priyanka Gandhi, GS- Gunjan Sardana, SK- Sonam Khera, RC- Rashmi Chawla, KS- Kalpana Sheokand, LR- Lalit Rai, SG- Shailender Gupta, DNC- Dr. Naresh Chauhan (COMPUTER), MB- Mamta Bansal (ENGLISH)										

**ECE-4 (S1)**

LEC T	1	2	3	4	5	6	7	8	9	10
TIME		9:15-10:10	10:10-11:05	11:05-12:00	12:00-12:55	12:55-1:50	1:50-2:45	2:45-3:40	3:40-4:35	4:35-5:30
MON	ELECTRONICS PROJECT WORKSHOP-II (EL -2 W/S)/(CONTROL SYSTEM LAB)								<b>MENTOR- MENTEE PERIOD</b>	
TUE		A&DC (GS)	S&S (KS)	MMC (SD)	CA (NS)	LUNCH	AC (VR)	BIO (SS)	ETC (ShD)	
WED		AC (VR)	ETC (ShD)	ETC (ShD)	S&S (KS)		AC (VR)	CA (NS)	BIO (SS)	
THU		CA (NS)	A&DC (GS)	MMC (SD)	MMC (SD)	LUNCH	S&S (KS)	A&DC (GS)	BIO (SS)	
FRI		A&DC LAB.(G1) (COMM2 W/S) (GS, KS ) MMC LAB.(G2) (MPI W/S)(SD, NY) AC LAB.(G3) (COMM1 W/S)(VR, SJ)		AC LAB.(G1) (COMM1 W/S) (VR, NY) A&DC LAB.(G2) (COMM2 W/S) (GS, KS) MMC LAB.(G3) (MPI W/S)(SD, RC)		LUNCH	MMC LAB.(G1) (MPI W/S) (SD, RC) AC LAB.(G2) (COMM1 W/S) (VR, NY) A&DC LAB.(G3) (COMM2 W/S) (GS, KS)		<b>LIBRARY PERIOD</b>	

GS- Gunjan Sardana, SD- Sangeeta Dhall, RC- Rashmi Chawla, NY- Nisha Yadav, PG- Priyanka Gandhi, SJ- Sheilza Jain, RT- Rohit Tripathi, ShD: SHIVANI DAGAR, KS- Kalpana Sheokand, NS- Nittin Sachdeva, VR- Vinod Rathore, SS-SONIA SARASWAT

EE (IOT)-4 (S1, T4, T5)										
LEC T	1	2	3	4	5	6	7	8	9	10
TIME		9:15-10:10	10:10-11:05	11:05-12:00	12:00-12:55	12:55-1:50	1:50-2:45	2:45-3:40	3:40-4:35	4:35-5:30
MON (S1)		MI (SANDEEP)	AEC (AA1)	PCS (PRATIBHA)	EW (NG)	LUNCH	BIO (DrS)	BIO (DrS)	DSA (BB)	
TUE	ELECTRONICS PROJECT WORKSHOP-II (EL -2 W/S)/(CONTROL SYSTEM LAB)									
WED (T5, T4)		EW (NG) (T5)	AEC (AA1) (T5)	AEC (AA1) (T5)	MI (SANDEEP) (T5)	LUNCH	DSA (BB) (T4)	PCS (PRATIBHA) (T4)	MENTOR- MENTEE PERIOD	
THU		MI LAB.(G1) (PCI W/S) (AA2, VR) AEC LAB.(G2) (COMM-1 W/S)(AA1, SK) EW LAB.(G3) (MWR LAB)(NG,SJ) COMM. SYS. LAB.(G4) (COMM-2 W/S) (SJ1, SHWETA)	EW LAB.(G1) LAB)(NG,SJ) (MWR MI LAB.(G2) (PCI W/S)(AA2, VR) COMM.SYS.LAB.(G3) (COMM-2 W/S) (SJ1, SHWETA) AEC LAB.(G4) (COMM-1 W/S) (AA1, SK)		LUNCH	AEC LAB.(G1) 1 W/S)(AA1, BK) (COMM- COMM. SYS. LAB.(G2) (COMM-2 W/S) (SJ1, ANJALI) MI LAB.(G3) W/S)(AA2, RT, PD) LAB.(G4) LAB)(NG,SJ)		COMM. SYS. LAB.(G2) (COMM-2 W/S) (SK, ANJALI) EW LAB.(G2) (MWR LAB)(NG, SJ) AEC LAB.(G3) (COMM-1 W/S)(AA1, BK) MI LAB.(G4) (PCI W/S)(AA2, PD)		
FRI (S1)		DSA (BB)	MI (SANDEEP)	PCS (PRATIBHA)	EW (NG)	LUNCH	DSA (BB)	BIO (DrS)	LIBRARY PERIOD	
AA2- Archana Aggrwal, AA2- Archana Aggarwal, BK- Balkishan, NG- Neetu Gupta, SJ- Sheilza Jain, VR- Vinod Rathore, KS- Kalpana sheokhand, LR- Lalit Rai, BB- Bharat Bhushan, PD- Pradeep Dimri, SK- Sonam Khera, SJ1- Sunil Jadav, DrS- Dr. SAKSHI										

**ENC-4 (T3, T4, T5)**

LEC T	1	2	3	4	5	6	7	8	9	10
TIME		9:15-10:10	10:10-11:05	11:05-12:00	12:00-12:55	12:55-1:50	1:50-2:45	2:45-3:40	3:40-4:35	4:35-5:30
MON (T4)		TS&S (NY)	DC (KS)	DSD (SD)	DSD (SD)	LUNCH	AEC LAB.(G1) LAB.) (BK, DS) LAB.(G2) (NS, AMIR) DS LAB.(G3) LAB) (SG, DIKSHANT) MPI LAB.(G4) W/S) (SD, RITIKA)	(MWR DSD (VLSI LAB) (COMP (MPI	MPI LAB.(G1) (MPI W/S) (SD, RC, RITIKA) AEC LAB.(G2) LAB.) (BK, DS) DSD LAB.(G3) LAB) (NS, PK) DS LAB.(G4) LAB)(SG,MK)	(MWR (VLSI (COMP
TUE (T3)		MPI(MV)	AEC (BK)	DS (SG)	TS&S (NY)	LUNCH	DS LAB.(G1) (COMP LAB)(SG, DIKSHANT) MPI LAB.(G2) W/S) (MV, RC) LAB.(G3) LAB.) (BK, AA1) DSD LAB.(G4) (VLSI LAB)(PK, AMIR)	(MPI AEC (MWR	DSD LAB.(G1) LAB) (NS, PK) LAB.(G2) LAB) (SG, PG) (MPI W/S) (MV, RC) AEC LAB.(G4) LAB.) (BK, AA1)	(VLSI DS (COMP MPI LAB.(G3) (MWR
WED	ELECTRONICS PROJECT WORKSHOP-II (EL -2 W/S)/(CONTROL SYSTEM LAB)									
THU (T5, T4)		DSD (SD) (T5)	DS (SG) (T5)	DC (KS) (T5)	MPI (MV) (T5)	LUNCH	BIO(AG) (T4)	TS&S (NY) (T4)	DC (KS) (T4)	
FRI (T3, T5)		AEC (BK) (T3)	DS (SG) (T3)	BIO (AG) (T3)	BIO (AG) (T3)	LUNCH	MPI (MV) (T5)	AEC (BK) (T5)		
MV- Munish Vashishath, SG- Shailender Gupta, AA1- Archana Agarwal, PK- Prashant Kumar, KS- Kalpana Sheokhand, NS- Nitin Sachdeva, RC- Rashmi Chawla, NY- Nisha Yadav, SD- Sangeeta Dhall, BK- BalKrishan, MK- Manju Kumari, PG- Priyanka Gandhi										