

SYNOPSIS

on

SOLAR POWER GENERATION FOR HOME BASED ON SOLAR PHOTOVOLTAIC CELL



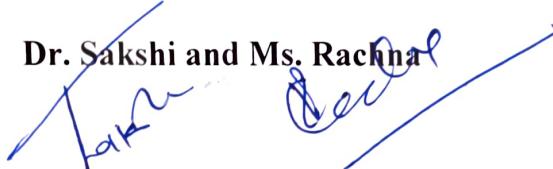
submitted by

ALOK KUMAR (21001518001)

(M-tech 2nd sem.)

Under the supervision of

Dr. Sakshi and Ms. Rachna

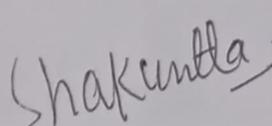


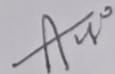
**Department of Electrical Engineering
Faculty of Engineering and Technology
J.C. Bose University of Science and Technology, YMCA,
Faridabad, Haryana-121006**

CERTIFICATE

This is to certify that the project entitled "**DESIGN AND SIMULATION OF ELECTRIC VEHICLE FOR ANALYZE THE OPTICAL PERFORMANCE & ENERGY CONSUMPTION**" submitted to Department of Electrical Engineering, Faculty of Engineering and Technology, J.C. Bose University of Science and Technology, YMCA, Faridabad, Haryana-121006 by **Mr. PRIYANSHU SINGLA (21001518002)** in partial fulfilment of the requirement for the SECOND SEMESTER of Degree of MASTER OF TECHNOLOGY in POWER ELECTRONICS AND DRIVES, is a bonafide work carried out by him/her under my supervision and guidance. This project work comprises of original work and has not been submitted anywhere else for any other degree to the best of my knowledge.

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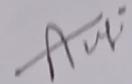
This is to certify that the project entitled "**CASCADED H BRIDGE MULTILEVEL INVERTER BASED ON ARDUINO MEGA**" submitted to Department of Electrical Engineering, Faculty of Engineering and Technology, J.C. Bose University of Science and Technology, YMCA, Faridabad, Haryana-121006 by **Mr. SHIVA SINGH (21001518003)** in partial Fulfilment of the requirement for the **SECOND SEMESTER** of **DEGREE** of **MASTER OF TECHNOLOGY** in **POWER ELECTRONICS AND DRIVES**, is a Bonafide work carried out by him under my supervision and guidance. This project work comprises of original work and has not been submitted anywhere else for any other degree to the best of my knowledge.



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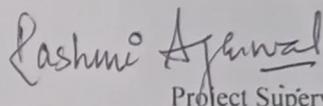


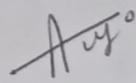
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CERTIFICATE

This is to certify that the project entitled "**BATTERY MANAGEMENT SYSTEM FOR ELECTRIC VEHICLE**" submitted to Department of Electrical Engineering, Faculty of Engineering and Technology, J.C. Bose University of Science and Technology, YMCA, Faridabad, Haryana-121006 by **Mr. VINAY RATHI (21001518004)** in partial Fulfilment of the requirement for the second semester of degree of **MASTER OF TECHNOLOGY** in **POWER ELECTRONICS AND DRIVES**, is a Bonafede work carried out by him under my supervision and guidance. This project work comprises of original work and has not been submitted anywhere else for any other degree to the best of my knowledge.

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