



**J.C. Bose University of Science & Technology, YMCA, Faridabad, Haryana**  
**(A Haryana State Government University)**  
(Established by Haryana State Legislative Act No. 21 of 2009 & Recognised by UGC Act 1956 u/s 22 to Confer Degrees)  
**Accredited 'A+' Grade by NAAC**

## **DEPARTMENT OF ELECTRONICS ENGINEERING**

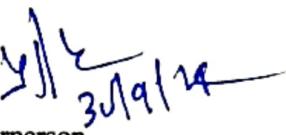
### **NOTICE**

Date : 30.09.2024

#### **Ph.D. Pre-submission Seminar**

Ph.D. Pre-submission seminar on title “Analysis and Design of Embedded System Based Reconfigurable Circuit for Signal Processing” of Mr. Naresh Kumar (University Regn. No. 09-YMCAU-2037, Roll No. 17001904004), Scholar in the Department of Electronics Engineering is to be held on 10.10.2024 at 11:30 A.M. in the offline mode in Conference Hall of the Department of Electronics Engineering.

All the faculty members, research scholars and students are invited to attend the seminar.

  
Chairperson

Chairperson  
(Electronics Engineering)  
Dept. of Electronics Engineering  
J.C. Bose University of Science  
and Technology, YMCA, Faridabad

CC:

1. PA to VC for kind information of Hon'ble VC
2. PA to Registrar for kind information of Registrar
3. Dean of Institutions
4. Director, R&D
5. Dean, Academics
6. Dean, FoET
7. Chairperson CE / IT/ CA / ME / MBA / EL / Phy. / Chemistry / Eng ./ Maths / Civil / EVS / JMC
8. Mr. Naresh Kumar, Research Scholar
9. Supervisor
10. Co-Supervisor
11. Ph.D. Coordinator
12. Expert Committee
13. Mr. Ankit Pannu, Web-Manager (Please upload on University Website)