



J C BOSE UNIVERSITY OF SCIENCE AND TECHNOLOGY, YMCA

(A Haryana State Government University)
(Established by Haryana State Legislative Act No. 21 of 2009 &
Recognized by UGC Act 1956 u/s 22 to Confer Degrees)
Accredited 'A+' Grade by NAAC

SPOT ADMISSION FOR M.TECH. PROGRAMME 2023–2024 on 28.8.23 (9AM to 4 PM)

DEPARTMENT OF ELECTRONICS ENGG.

DISCIPLINES

- M.Tech (Electronics & Communication)
- M.Tech(VLSI Design)

HIGHLIGHTS

- High end servers with NVIDIA graphics processors for AI & ML.
- Proteus Design software for embedded/IoT system design and simulation.
- Embedded and IoT boards with necessary sensors and actuators.
- Vivado Design Suite, Visual TCAD tool and Zynq SOC
- FPGAs for VLSI design and development. MATLAB bundle with all tool boxes.
- Labview software with USRP: NI Universal Software Radio Peripheral, HFSS, VNA.

Eligibility

B.E. / B.Tech or equivalent degree in Electronics & Communication / Electronics & Telecommunication Engg. / Electronics Engg. / Electrical & Electronics Engg. / Applied Electronics & Instrumentation Engg. / Electronics & Instrumentation & Control Engg. / Electrical Engg. / Instrumentation & Control Engg. / Instrumentation Engg. / Control Engg. / Biomedical Engg. / Mechatronics / or in relevant Branch or M.Sc. (Electronics) / M.Sc. (Physics) with specialization in Electronics) from a recognized University / Institution with at least 50% marks in aggregate (45% marks for SC candidates of Haryana)

WHY TO JOIN?

- Doctrate & Experienced Faculties.
- Curriculum as per Industry & AICTE standard.
- Opportunity for Stipend Based Internship in Core Industries with Pre-Placement Offers (PPOs).
- GATE Scholarship for eligible candidate.
- Effective Teaching Pedagogy including Project Based Learning.
- Opportunity to work on Funded Projects at Different Centers of Excellence in the Department.
- Well Equipped Labs with Industry Standard Hardware and Software Tools.
- Regular Interaction with strong Alumnus for Career Planning, Higher Studies & Research Opportunities.

Memo. of Understanding

- Centre of Excellence of Godrej Home Appliances
- MoU with established Central Research Institutions such as CEERI Pilani, SCL Mohali, IUAC Delhi etc.
- Academic MOU with Arctic University of Norway.

MAJOR RECRUITERS

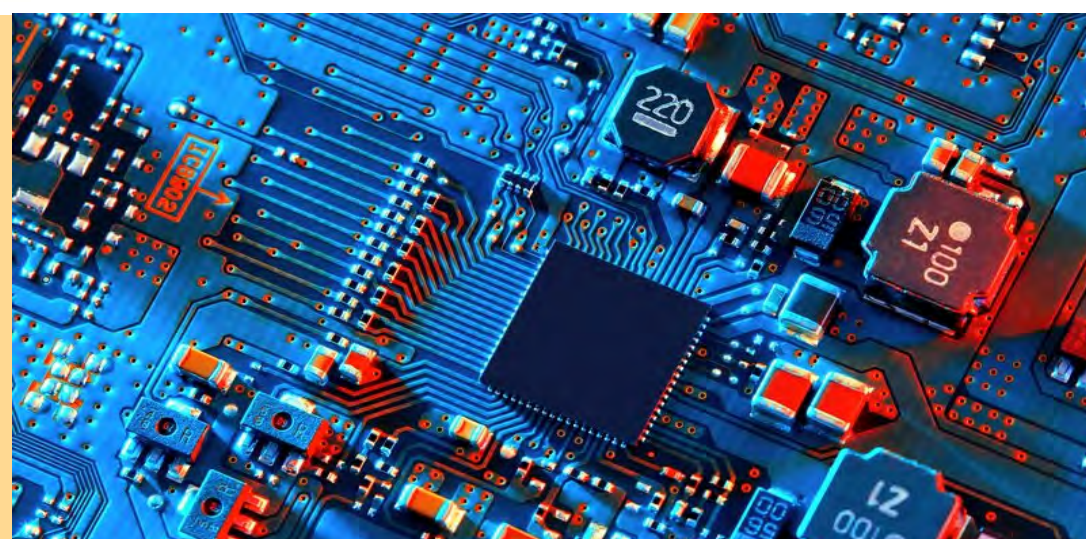


L&T Infotech



MAJOR RESEARCH AREAS

- VLSI DESIGN, SEMICONDUCTOR DEVICES
- SIGNAL PROCESSING, IMAGE PROCESSING
- WIRELESS SENSOR NETWORK
- ADHOC NETWORKING
- INTERNET OF THINGS



NIRF Ranking (Engg. Prog): 101-150 (2023)

State of Art Flexible Electronics Lab

Multiple Curricular and Co-curricular Activities for overall Development of Student

Admission on the basis of GATE Score/ Percentage in qualifying Exam.

Contact

Dr Pradeep Dimri Chairperson,

Dept. of Electronics Engg

J.C. Bose Univ. of Sc. & Tech. YMCA Fbd

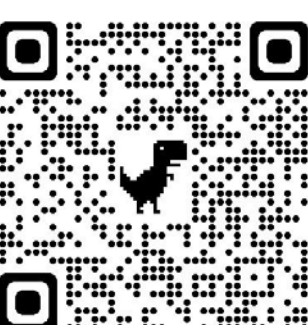
Email ID: chairperson.ece@jcboseust.ac.in

Contact Ms. Gunjan 9999509869 for more details.

For more information regarding admission Process visit:

<https://jcboseust.ac.in/admissions-courses>

Fresh application will be submitted on 28/08/2023 from 9 AM to 4 PM in the Electronics Engineering Dept with the application fee of Rs. 1050/- (Rs 525/- for SC candidates of Haryana) by DD in favour of Registrar JC Bose UST, Faridabad.





GENERAL INSTRUCTION & SCHEDULE FOR

SPOT COUNSELLING OF M.TECH. ECE/VLSI ENGINEERING

DATED 28.08.2023 (MONDAY)

SCHEDULE & GENERAL INSTRUCTIONS:

1. Admission will be done as per COVID guidelines issued by the Govt. Wearing mask and keeping social distance is mandatory for all students reporting for admission.
 2. Eligibility Criteria: B.E. / B.Tech or equivalent degree in Electronics & Communication / *Electronics & Telecommunication Engg. / Electronics Engg. / Electrical & Electronics Engg. / Applied Electronics & Instrumentation Engg. / Electronics & Instrumentation & Control Engg. / Electrical Engg. / Instrumentation & Control Engg. / Instrumentation Engg. / Control Engg. / Biomedical Engg. / Mechatronics / or in relevant Branch or M.Sc. (Electronics) / M.Sc. (Physics) with specialization in Electronics) from a recognized University / Institution with at least 50% marks in aggregate (45% marks for SC / ST candidates of Haryana only).*
 3. *All originals documents will be required at the time of Manual / Institute Level counseling for verification.*
 4. Candidate must have to attend the counseling physically and also to present themselves in person before the admission committee for the allotment of seat. Physically attend the counseling.
 5. Eligibility of the candidate for the admission is subjected to verification and submission of required documents at the time of admission.
 6. If a candidate reports late for the document verification during counseling, he/she will be eligible only for the available seats at his/her turn after verification of documents.
- Candidates has to deposit the fee amount *Rs. 57086/-*. Fee can be deposited in the form of Demand Draft (drawn in favour of "Registrar J.C. Bose UST, YMCA Faridabad") **on the spot** on offering the seat for admission. Payment can also be made by Debit/Credit card with applied charges.
- **Venue of reporting for Counseling:**

Control Lab, 1st floor, Electronics Engineering Department, J.C. Bose University of Science and Technology, YMCA Faridabad)

- **Schedule for reporting on 28.08.2023 (MONDAY): 09:00 am-04:00 pm**

➤ **Documents required for Candidates**

1. Printout of application form
2. Graduation/Post-graduation mark sheets and degree
3. 10+2 Marksheet
4. 10th/Matriculation/High School Certificates.
5. Character Certificate/ Gap Year affidavit.
6. Medical Fitness Certificate
7. Four Passport size color photographs
8. Anti-ragging affidavits from Student and Parent.
9. Aadhar Card
10. Copy of Parivar Pahchan Patra
11. Migration certificate in original (wherever applicable).