



J. C. Bose University of Science and Technology, YMCA, Faridabad

(formerly YMCA University of Science and Technology)
A State Govt. University established vide State Legislative Act. No. 21 of 2009

SECTOR-6, FARIDABAD, HARYANA-121006

ESTABLISHMENT BRANCH

Ph:0129-2310158, 2242143 (Fax) Email: dr.ymcaust@gmail.com web: www.ymcaust.ac.in



Ref: Estb/Advt. 05-2020/ 673

Dated: 24/02/2021

NOTICE

Subject: Interview for the posts of Associate Professor in Electrical Engineering against Advt. No-05/2020

Reference: the posts of Associate Professor advertised vide Advt. No. 05/2020 & the applications received for these posts.

All concerned are hereby informed that the Interviews for the posts of Associate Professors is scheduled to be held as follows:

Subject	Date & time of Interview
Associate Professor (Electrical Engg)	March 3, 2021 at 9 AM

All eligible applicants have been shortlisted for Interview for above mentioned post. The scrutiny reports are attached herewith at Annexures- A & B ***These eligible/ shortlisted applicants are required to report at above mentioned date & time in Multi Media Centre for Interview & Presentation on teaching skills (5-10 minute ppt), along with all original documents/ testimonials for verification in support of his/her eligibility.*** It is reiterated that the eligibility of above shortlisted applicants is purely provisional (as it is based on the information/ document filled in the application form) and subject to verification of documents before the Interview. No applicant will be allowed to appear in the Interview without verifying the eligibility.

In case of employed in Govt./Semi-Govt./PSU etc, it will require producing No Objection Certificate from present employer, if not applied through proper channel. Please note that no TA/DA would be admissible for appearing in the Interview.

- All Notice Boards
- Web Administrator: to upload on University website
- All applicants via email

24/2/2021
REGISTRAR
24/2/2021

Encl: as above

ANNEXURE-^uA

SCRUTINY OF APPLICATIONS FOR THE POST OF ASSOCIATE PROFESSOR (ELECTRICAL ENGG) AGAINST ADVT. NO. 05/2020												
S.No	Application No.	B.Tech (Percentage)	M.Tech (Percentage)	First class in either in B.Tech or M.Tech	Whether Ph.D. (As per 2009 UGC guidelines)	Whether candidate is having PhD degree in relevant field and first class or equivalent at either bachelor's or Master's level in the relevant branch	Whether candidate is having 8 years of experience	Whether candidate is having Post PhD Experience of two years or not	Whether candidate is having atleast 6 research publications in SCI journals/UGC/AICTE approved UGC journal	Whether candidate is scoring the minimum API marks	Eligible or not	Remarks
1	05/2020_010_552	65.97%	95%*	Yes	Yes	No	Yes	Yes	yes	Yes	No*	M.Tech (Material Engg.) of candidate is not in relevant branch as per AICTE guidelines 2018
2	05/2020_010_943	74.32%	92.63%	Yes	Yes	No	Yes	Yes	yes	Yes	No*	1.Ph.D. (Electronics Engg.) of candidate is not in relevant field as per AICTE guidelines 2018
3	05/2020_010_584	69.49%	78.47%	Yes	Yes	Yes	Yes	Yes	yes	Yes	Yes	Required to produce NOC from the government employer at the time of interview
4	05/2020_010_961	71.54%	70.30%	Yes	No	Yes	Yes	Yes	Yes	Yes	Yes	Compliance certificate of Ph.D. according to UGC 2009 guidelines is to be provided
5	05/2020_010_911	68.94%	75.63%	Yes	Yes	No	Yes	Yes	No		No*	1.Less than 6 research publications in SCI journals/UGC/AICTE approved UGC journal 2.B.Tech (Instrumentation and control Engg.) of candidate is not in relevant field as per AICTE guidelines 2018
6	05/2020_010_577	68.43%	76%	Yes	Yes	Yes	Yes	yes	yes	yes	YES	1.Required to produce NOC at the time of interview
7	05/2020_010_914	72.43%	71%	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	
8	05/2020_010_924	73.26%	74.93%	Yes	Yes	Yes	Yes	No	No		No*	Lack of post Ph.D. Experience
9	05/2020_010_555	65.15%	76.28%	Yes	compliance certificate of 2009 UGC regulation is to be attached	Yes	Yes	Yes	Yes	Yes	Yes	1.Required to produce NOC at the time of interview 2.compliance certificate of 2009 UGC regulation is to be attached
10	05/2020_010_566	60.47%	71.78%	Yes	compliance certificate of 2009 UGC regulation is to be attached	yes	NO	Yes	yes	Yes	No*	Expreience is less than 8 years. Teaching Assistant during Ph.D is not counted.

Note: 1. The eligibility mentioned above is purely provisional and subject to document verification before the Interview. It is made clear that no applicant will be allowed to appear in the Interview, without establishing his/her eligibility as per eligibility conditions advertised.

2. Due care has been observed in preparing above report but the University reserves the right to remove the discrepancy/inadvertenet error, if any, at any stage.

ANNEXURE = "B"

SCRUTINY OF APPLICATIONS FOR THE POST OF PROFESSOR IN ELECTRICAL ENGG AGAINST ADVT NO. 05/2020

S.No	Application No.	B.Tech (Percentage)	M.Tech (Percentage)	First class in either in B.Tech or M.Tech	Whether Ph.D. (As per 2009 UGC guidelines)	Whether candidate is having PhD degree in relevant field and first class or equivalent at either bachelor's or Master's level in the relevant branch	Whether candidate is having 8 years of experience	Whether candidate is having Post PhD Experience of two years or not	Whether candidate is having atleast 6 research publications in SCI journals/UGC/AICTE approved UGC journal	Whether candidate is scoring the minimum API marks	Eligible or not	Remarks
1	05/2020_009_263	58.33% (A.M.I.E)	78.47%	Yes	compliance certificate of 2009 UGC regulation is to be attached	Yes	Yes	Yes	Yes	Yes	No	Advertisement post is for SC and applicant is of General Category
2	05/2020_009_199	67%	75.50%	Yes	Yes	Yes	yes	yes	yes	yes	No	Advertisement post is for SC and applicant is of General Category

Note: 1. The eligibility mentioned above is purely provisional and subject to document verification before the Interview. It is made clear that no applicant will be allowed to appear in the Interview, without establishing his/her eligibility as per eligibility conditions advertised.

2. Due care has been observed in preparing above report but the University reserves the right to remove the discrepancy/inadvertent error, if any, at any stage.