Dr. Prashant Kumar

Associate Professor
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Skills

- VLSI Design
- Semiconductor Devices
- Career Guidance & Student Counselling – Proven track record in guiding students toward suitable career paths, aligning academic strengths with industry needs.
- Job Seeker Motivation & Engagement – Skilled at encouraging and inspiring job seekers to explore diverse career possibilities and maximize their potential.
- Industry Networking & Placement Support – Strong ability to connect students with industry professionals, fostering internships, training, and job opportunities.

Professional Summary

Associate Professor with 17 years of experience in VLSI Design curriculum development and delivery. Driven to contribute to program outcomes by facilitating engagement, supporting learning objectives, and fostering academic development. Served as Assistant Training and Placement Officer, honing expertise in student counselling, strategic career guidance, and industry collaboration, resulting in enhanced placement opportunities and stronger academia–industry linkages.

Work History

Associate Professor | Assistant Professor (2008 – Present)
J. C. Bose University of Science & Technology, YMCA Faridabad, Haryana

- Teaching undergraduate and postgraduate classes.

-Worked as Assistant Training and Placement Officer, built strong collaborations with industry partners to enhance student placement opportunities.

- Organized career awareness programs and placement drives.

- Assisted in connecting students with industry professionals for mentoring, internships and job placements.

- Designed and implemented structured career counselling sessions for students, helping them identify strengths, set goals, and explore suitable career options.

- Motivated and engaged job seekers through personalized mentoring, mock interviews, and employability skill workshops.

- Nodal officer for AICTE Parakh (Skill test for Engineering Students) and University Community College.

- IQAC team member for NAAC , NBA and NIRF.

- Sub Editor for the University Journal.

- Served as Assistant State Public Information Officer of the University.

- Member of Departmental Syllabus Committee.

Education

Ph.D. in Modelling of Junctionless Semiconductor Devices (2021)
J C Bose University of Science & Technology, YMCA Faridabad, Haryana

M.Tech. in VLSI Design (2006 – 2008)
G J U University of Science & Technology, Haryana

B.Tech. in Electronics and Communications Engineering (2000 – 2004)
M D University, Haryana

Accomplishments

- Significantly increased the number of recruiting companies visiting the university for placements through targeted outreach and relationship-building initiatives.
- Initiated career awareness programs that increased student participation in placement drives significantly.
- Published 8 papers in SCI/SCIE Journals.
- Published 6 papers in SCOPUS/ESCI International Journals of repute.
- Presented about 60 papers in different National/International Conferences across the country.
- Conducted 5 Faculty Development Programs.
- Conducted 3 session in the International Conferences of repute “DevIC 2023” and “DevIC 2025” at Government Engineering College, Kalyani, West Bengal and EDKCON 2024.
- Served as session chair in 5 International Conferences.

List of Publications (Journals & Conferences)

1. Neeraj Gupta, Rashmi Gupta, Rekha Yadav, **Prashant Kumar**, Lalit Rai, "A Temperature Dependent Analytical Model & Performance Evaluation of Triple-Material Dielectric-Pocket Gate-all-around MOSFET" Discover Applied Sciences, Vol 6 No 670, 2024**(ESCI/SCOPUS)**.
2. Neeraj Gupta, Rashmi Gupta, S. B. Gupta, Rekha Yadav and **Prashant Kumar**," Performance Investigation of a Dielectric Stacked Triple Material Cylindrical Gate All Around MOSFET (DSTMCGAA) for Low Power Applications" ECS Journal of Solid-State Science and Technology, Vol. 12, No. 1, 2023 **(SCIE)**.
3. Usha Dhankar, Sunita Dahiya, Rashmi Chawla, **Prashant Kumar**, Neeraj Gupta, "Numerical Simulation of Temperature Dependency on Performance of Solar PVC", silicon, 2022. **(SCI)**
4. **Prashant Kumar**, Munish Vashistha, Neeraj Gupta, Rashmi Gupta, "High-k Dielectric Double Gate Junctionless (DG-JL) MOSFET for Ultra Low Power Applications- Analytical Model", Silicon, 2022 **(SCI)**.
5. **Prashant Kumar**, Munish Vashistha, Neeraj Gupta, Rashmi Gupta, "Subthreshold Current Modeling of Stacked Dielectric Triple Material Cylindrical Gate All Around (SD-TM-CGAA) Junctionless MOSFET for Low Power Applications", Silicon, 2021 **(SCI)**.
6. Neeraj Gupta, **Prashant Kumar**, "Elicitation of Scattering Parameters of Dual-halo Dual- dielectric Triple-material Surrounding Gate (DH-DD-TM-SG) MOSFET for Microwave Frequency Applications", Advances in Electrical and Electronics Engineering, Vol. 19, No. 1, pp. 66-73, April 2021. **(ESCI)**
7. Neeraj Gupta, **Prashant Kumar**, Nitin Sachdeva, Tarun Sachdeva, Munish Vashistha, "Performance investigation of Dual-Halo Dual-Dielectric Triple Material Surrounding Gate MOSFET with High-K dielectrics for Low Power Applications", Journal of Semiconductor Technology and Science, Vol 20, No. 3, pp.1-8,June 2020. **(SCI)**
8. Neeraj Gupta, Rashmi Gupta, **Prashant Kumar** and Amit Sharma, "Performance Analysis of Noise in Dual Halo Dual Dielectric Triple Material Surrounding Gate MOSFET for RF Applications", International Journal of Nanoscience, Vol. 1, No. 1, pp. 1-4, 2019 **(Scopus)**.
9. **Prashant Kumar**, Munish Vashishath and P K Bansal, "Analytical Modeling and Simulation of Nanoscale Fully Depleted Dual Metal Gate SOI MOSFET" International Journal of Innovative Technology and Exploring Engineering, Vol 8 No 10, 2019 **[SCOPUS Journal]**
10. **Prashant Kumar**, Munish Vashishath and P K Bansal, "Comparative Analysis of FD-DMDG SON MOSFET over FD DMDG SOI MOSFET" Journal of Electronic Design Technology, Vol 9 No 1, 2018 **[UGC Approved Journal]**.
11. **Prashant Kumar**, Munish Vashishath and P K Bansal, "Study of Scaling of MOSFET on Various Electrical Characteristics using Silvaco TCAD Tool" Journal of Semiconductor Devices and Circuits, Vol 5, no 1, 2018 **[UGC Approved Journal]**.
12. **Prashant Kumar**, Munish Vashishath and P K Bansal, "An Investigation into

NMOS at 65nm using Silvaco TCAD", International Journal of Management, Technology and Engineering, Vol 8 No IX, 2018 **[UGC Approved Journal]**.

13. **Prashant Kumar**, Shikhar Gupta, Anjali Chawla "Comparative Analysis of Fully Depleted DMG SOI-MOSFET and SMG SOI-MOSFET", International Journal of Recent Research Aspects ISSN: 2349-7688, Vol. 2, Issue 2, June 2015, pp. 125-128 **[UGC Approved Journal]**
14. **Prashant Kumar**, Neeraj Gupta and Rashmi Gupta, "Comparative study of low pass filter using OPAMP and current conveyor", YMCAUST International Journal of Research, vol. 2, issue 2, 2014 **[UGC Approved Journal]**
15. **Prashant Kumar**, Pinki Rani, Munish Vashishath, "An Efficient Approach to Design 5 Bit Flash ADC", YMCAUST International Journal of Research, vol. 1, issue 2, 2013 **[UGC Approved Journal]**
16. **Prashant Kumar**, Priya yadav, Rashmi Chawla, Nishu Bishnoi "A Novel approach for implementation of Ripple Carry adder using clock zone based crossover" YMCAUST International Journal of Research, vol. 3, issue 2, 2015 **[UGC Approved Journal].**
17. Shanu Jain, **Prashant Kumar**, Vimlesh Singh, "Analysis of Self-Similar Antennas and its Applications" International Journal of Communication Systems and Network Technologies, 2020, VOL.9, ISSUE1,pp-1-9.

18. **Prashant Kumar**, Neeraj Gupta, Rashmi Gupta, Lalit Rai, Sandeep Sharma, "Machine Learning Assisted Sensitivity of Triple-Material Gate -Stack Gate-All-Around (TM-GS-GAA) MOSFET based Biosensor" 2025 Devices for Integrated Circuit (DevIC) 7-8 April, 2025, Kalyani, West Bengal, India **(Scopus Indexed Conference)**.
19. **Prashant Kumar**, Neeraj Gupta, Lalit Rai, " Future of Smart and Adaptive Devices using Flexible Electronics: A Review" Flexible Electronics: Real World Applications, March 2025, JCBUST Faridabad.
20. Lalit Rai, **Prashant Kumar** "AI and IoT for Smart Soil Monitoring" Flexible Electronics: Real World Applications, March 2025, JCBUST Faridabad.
21. **Prashant Kumar**, Neeraj Gupta, Rashmi Gupta, Lalit Rai, "Effects of Gate Metal Engineering on Electrical Characteristics of SOI MOSFET" 3rd IEEE Electron Devices Kolkata Conference 2024, Kolkata, India **(Scopus Indexed Conference).**
22. **Prashant Kumar**, Neeraj Gupta, Lalit Rai, " Impact of Gate Metal Engineering on the Electrical Performance of SOI MOSFETs" International Conference on Information Technology and Intelligence (ITI 2024), October 2024, DBRAIT, Andaman.
23. **Prashant Kumar**, Neeraj Gupta, Lalit Rai, "Comparative Analysis of Adiabatic Techniques for Multiplexer Design" International Conference on Information Technology and Intelligence (ITI 2024), October 2024, DBRAIT, Andaman.
24. **Prashant Kumar**, Neeraj Gupta, Lalit Rai and Sandeep Sharma "Investigation of Temperature Associated Performance Variations in Nanoscale Recessed Channel Double Gate Junctionless Transistor" 2nd International Conference on Emerging Technology and Sustainable Solutions ICETSS 2024, October 2024,

Chitkara University, Rajpura Punjab **(Scopus Indexed Conference).**

25. Neetu Gupta, **Prashant Kumar**, Neeraj Gupta and Lalit Rai "I Simulation & Analytical Model of Gate Stack Gate All Around MOSFET as Gas Sensor" 2nd International Conference on Emerging Technology and Sustainable Solutions ICETSS 2024, October 2024, Chitkara University, Rajpura Punjab **(Scopus Indexed Conference).**
26. Prashant Kumar, Neeraj Gupta, Lalit Rai "Adiabatic CMOS D Flip Flop: Design and Computation Model" International Conference on Recent Trends in Transport Processes (RTTP-2024), June 2024, NIT Hamirpur **(Scopus Indexed Conference).**
27. Lalit Rai and **Prashant Kumar** "Soil Productivity Estimation for Environmental Health Management" International Conference on Recent Trends in Transport Processes (RTTP-2024), June 2024, NIT Hamirpur **(Scopus Indexed Conference).**
28. Balkrishan, Neelam Bedwal, **Prashant Kumar**, Neeraj Gupta, Ved Prakash, "Performance Analysis of Double Gate MOSFET with High-k Dielectric", 2023 IEEE Devices for Integrated Circuit (DevIC), 7-8 April, 2023, Kalyani, India **(Scopus Indexed Conference)**
29. Lalit Rai, **Prashant Kumar**, Neeraj Gupta, Rashmi Gupta "Performance Analysis of Low Power Multiplexer for Communication System", 2023 IEEE Devices for Integrated Circuit (DevIC), 7-8 April, 2023, Kalyani, India **(Scopus Indexed Conference)**
30. **Prashant Kumar**, Lalit Rai, Neeraj Gupta, Rashmi Gupta "Design of Low power and High-Performance Decoder Using Carbon Nanotube Field Effect Transistor (CNTFET)", 2023 IEEE Devices for Integrated Circuit (DevIC), 7-8 April, 2023, Kalyani, India **(Scopus Indexed Conference)**
31. **Prashant Kumar**, Lalit Rai, Neeraj Gupta, Rashmi Gupta, "A Comparative Study of Various Adiabatic Techniques for Multiplexer" International Conference on Advanced Computing Techniques in Engineering & Technology, SKIT, Jaipur, 18-19 December, 2023 **(Scopus Indexed Conference).**
32. Lalit Rai, **Prashant Kumar**, Neeraj Gupta, Rashmi Gupta, "Simulation Analysis of Fuzzy Controller for Water Level of Boiler" International Conference on Advanced Computing Techniques in Engineering & Technology, SKIT, Jaipur, 18-19 December, 2023 **(Scopus Indexed Conference).**
33. Lalit Rai, **Prashant Kumar**, Neeraj Gupta, Nitin Goel, "A Mathematical Model of Steam Drum Boiler and Its Simulink Model for Power Plant" International Conference on Emerging Trends in Engineering, Science Technology and Buisness 2023, EIT, Faridabad, Haryana, May 2023.
34. Lalit Rai, **Prashant Kumar**, Neeraj Gupta, Vinod Rathor "Simulation Analysis of Fuzzy Controller for Water Level of Boiler" International Conference on Emerging Trends in Engineering, Science Technology and Buisness 2023, EIT, Faridabad, Haryana, May 2023.
35. **Prashant Kumar**, Lalit Rai, Neeraj Gupta, Rashmi Gupta, "Diminished Short Channel Effects (SCEs) in Junction Less Double Gate(JLDG) MOSFET" 2022 IEEE Electron Device Kolkata Conference (EDKCON-2022), Kolkata, Nov 2022 **(Scopus Indexed Conference).**

36. Lalit Rai, **Prashant Kumar**, Neeraj Gupta, Rashmi Gupta, "Performance Analysis of Adiabatic CMOS Interface for Low Power Application" 2022 IEEE Electron Device Kolkata Conference (EDKCON-2022), Kolkata, Nov 2022 **(Scopus Indexed Conference).**
37. **Prashant Kumar**, Lalit Rai, Neeraj Gupta, Rashmi Gupta, "Design and implementation of array multiplier using compressor for low power" 2022 International Conference for Advancement in Technology (ICONAT), GU, Goa, India, Jan 2022 **(Scopus Indexed Conference).**
38. Lalit Rai, **Prashant Kumar**, Neeraj Gupta, Rashmi Gupta, "Design of an Ultra-Low Power CMOS ADC using Threshold Inverter Quantization for Communication System" 2022 International Conference for Advancement in Technology (ICONAT), GU, Goa, India, Jan 2022 **(Scopus Indexed Conference).**
39. **Prashant Kumar**, Lalit Kumar, Neeraj Gupta, Rashmi Gupta, "Design of an Ultra-Low Power CMOS ADC using Threshold Inverter Quantization for Communication System", International Conference for Advancement in Technology at Rajarambapu Institute of Technology Rajaramnagar, Uran Islampur, Maharashtra, 2022 **(Scopus Indexed Conference)**.
40. **Prashant Kumar**, Lalit Kumar, Neeraj Gupta, Rashmi Gupta, "Design and Implementation of Array Multiplier using Compressor for Low Power", International Conference for Advancement in Technology at Rajarambapu Institute of Technology Rajaramnagar, Uran Islampur, Maharashtra, 2022 **(Scopus Indexed Conference).**
41. **Prashant Kumar**, Neeraj Gupta, Rashmi Gupta, "Performance analysis of Fully depleted SOI MOSFET incorporating Dielectric Engineering", 3rd International Conference on Future of Engineering Systems and Technologies at Indira Gandhi Technical University for Women, Delhi, 2021 **(Scopus Indexed Conference)**.
42. **Prashant Kumar**, Munish Vashishath, Neeraj Gupta, "Analytical Modeling and Performance Analysis of Surface Potential for Junctionless MOSFET", Journal of Physics: Conference Series on International Conference on Computing, Communication, Electrical and Biomedical Systems at KPR Institute of Engineering and Technology, Coimbatore, Tamilnadu, India, 2021 **(Scopus Indexed Conference)**.
43. **Prashant Kumar**, Neeraj Gupta, Lalit Rai, Munish Vashishath and Rashmi Gupta, "Performance Analysis of Junction Less Double Gate (JL DG) MOSFET for Low Power" International Conference on Innovations in Smart Technology, Advanced Materials and Communication Engineering (ISTAMCE) AMITY University, Gwalior, MP, June 2021.
44. **Prashant Kumar**, Neeraj Gupta, Rashmi Gupta, Lalit Rai and Munish Vashishath, "Performance Investigation of Fully Depleted SOI MOSFET with Various Dielectric Materials" International Conference on Innovations in Smart Technology, Advanced Materials and Communication Engineering (ISTAMCE) AMITY University, Gwalior, MP, June 2021.
45. **Prashant Kumar**, Neeraj Gupta, Lalit Rai, Rashmi Gupta, and Munish Vashishath, "Simulation and Analysis of Multiplexer using Various Design Methodology" International Conference on Innovations in Smart Technology,

Advanced Materials and Communication Engineering (ISTAMCE) AMITY University, Gwalior, MP, June 2021.

46. Neeraj Gupta, **Prashant Kumar**, Rashmi Gupta "Mitigation of Short Channel Effects in Dual-Material Gate (DMG) SOI MOSFET" International Conference on Innovations in Smart Technology, Advanced Materials and Communication Engineering (ISTAMCE) AMITY University, Gwalior, MP, June 2021.
47. Prince, **Prashant Kumar**, Lalit Rai, Sunil Jadav "Design & Analysis of Low Power Full Adders using GDI, SBT, TG & Combined Techniques" International Conference on Innovations in Smart Technology, Advanced Materials and Communication Engineering (ISTAMCE) AMITY University, Gwalior, MP, June 2021.
48. Somvir, **Prashant Kumar** and Lalit Rai "Design and Analysis of Array Multiplier and M:2 Compressor-based Multiplier" International Conference on Innovations in Smart Technology, Advanced Materials and Communication Engineering (ISTAMCE) AMITY University, Gwalior, MP, June 2021.
49. **Prashant Kumar**, Neeraj Gupta, Lalit Rai, Rashmi Gupta, and Munish Vashishath, "A Simulation Perspective for Novel Characteristics of DH- DD-TM-SG MOSFETs" 8th International Symposium on Fusion of Science and Technology (ISFT-2020), JCBUST, Faridabad, Haryana, Jan 2020
50. **Prashant Kumar**, Munish Vashishath, Neeraj Gupta, Rashmi Gupta," Low Power Sub- threshold Domino AND Gate", Journal of Physics: Conference Series on International conference on Future of Engineering Systems and Technologies at Greater Noida, 2020 **(Scopus Indexed Conference)**
51. Neeraj Gupta, Rashmi Gupta, and **Prashant Kumar**, "Power Efficient Combinational Circuits using Reversible Gate", proceedings of International Conference on International Conference on Machine Intelligence and Smart Systems at Rustamji Institute of Technology, Border Security Force Academy, Tekanpur, Gwalior on September 24-25, 2020 **(Scopus Indexed Conference)**.
52. **Prashant Kumar,** Lalit Rai and Neeraj Gupta "A review of Conventional and junction less MOSFET using TCAD simulation" 3rd National Conference Medical Instrumentation, Biomaterials and Signal Processing 2020. DCRUST, Murthal, Sonipat 2020.
53. **Prashant Kumar,** Lalit Rai and Neeraj Gupta "FinFET analysis and structural process Mechanism" 3rd National Conference Medical Instrumentation, Biomaterials and Signal Processing 2020. DCRUST, Murthal, Sonipat 2020.
54. Neeraj Gupta, Rashmi Gupta, **Prashant Kumar** and Munish Vashisth, "2D simulation study of novel attributes of dual-halo dual-dielectric triple-material surrounding gate MOSFET", proceedings of International Conference on Advancements in Engineering And Technology at Bhai Gurudas institute of technology, Sangrur on 15-16 March, 2019.
55. Neeraj Gupta, Rashmi Gupta, **Prashant Kumar** and Amit Sharma, "Noise analysis of dual halo dual dielectric triple material surrounding gate MOSFET for RF applications", proceedings of Devices for integrated circuits at Govt. Engg. College Kalyani on 23-24 March 2019 **(Scopus Indexed Conference)**.
56. Neeraj Gupta, Rashmi Gupta, **Prashant Kumar** and Ganesh Gupta, "Electric field modeling and critiques of Dual-Halo Dual-Dielectric Triple-Material

Surrounding-gate MOSFET", proceedings of International Conference on Intelligent Computing and Smart Communication at THDC- Tihri on April 19-21, 2019 **(Scopus Indexed Conference)**.

57. **Prashant Kumar**, Neeraj Gupta and Rashmi Gupta, "Surface potential modelling and critiques of Dual-Halo Dual- Dielectric Triple- Material Surrounding- Gate MOSFET" International Conference on Materials and Energy (ICME 2019), CGPC, July 2019.
58. **Prashant Kumar** and Raj Kumari "Analysis and Measurement of Various Parameters of OP-AMP" Innovative Research in Language Science and Management, DAVCC Faridabad Oct. 2019.
59. **Prashant Kumar**, Kanika Jain, Munish Vashishath, P K Bansal, "Design and Analysis of 90nm nMOSFET for Lower Leakage" International Conference on Recent research and Innovation in Social Science & Education (RISE 2018), Osmania University, Hyderabad, India, 2018.
60. **Prashant Kumar**, Neeraj Gupta "Charge Restoring and Leakage Reduction Techniques in Domino Circuit" 1st International conference on New Frontiers in Engineering, Science and technology-2018, AITM Palwal, Jan 2018.
61. **Prashant Kumar**, Munish Vashishath and P K Bansal "Simulation of NMOS Transistor using Silvaco TCAD at 65nm Technology" International Conference on Materials Research and Technology (ICMRT-2017), Aggarwal College, Ballabhgarh, Haryana, July 2017.
62. **Prashant Kumar**, Anjali Chawla, Munish Vashishath and P K Bansal, "An Investigation of Different Gate Dielectric Material for FD-SOI MOSFET", Computing for Sustainable Global Development (INDIACom-2017) , BVI New Delhi, March 2017 **(Scopus Indexed Conference)**.
63. **Prashant Kumar**, Neeraj Gupta, Ashutosh Gupta, Rashmi Gupta and Janak B Patel, "Mitigating parameter variation in MOSFETS" proceedings of international conference on "Soft computing and Problem solving" at IIT Roorkee campus Saharanpur on 19-20Dec ,2015 **(Scopus Indexed Conference).**
64. Shikhar Gupta, **Prashant Kumar,** Munish Vashishath "Parameters Extraction of 200nm NMOS Transistor using Silvaco TCAD Tools" Second National Conference on Machine Intelligence and Research Advancement (NCMIRA-2015), BPS Sonipat, March 2015.
65. **Prashant Kumar,** Nidhi Sharma, "Conventional CMOS Logic Versus ADIABATIC Logic Circuit for Power Reduction" 2nd National conference on Emerging trends in Electronics & Information Technology AITM Palwal, Haryana, INDIA, April, 2012.
66. **Prashant Kumar,** Khusboo Sagar, Nisha Goel, "Design & comparison of Booth's & Robertson's Algorithms multiplier on various parameter" International conference on VLSI, MEMS & NEMS, AMITY University, Noida, Uttar Pradesh, Jan 2012.
67. **Prashant Kumar**, Nidhi Sharma, Ritu Sharma "Different Adiabatic Technique for Power Reduction" National conference on New paradigms in Electronics, Communication and Computing, Echelon Institute of Technology, Faridabad, Haryana, May 2012.
68. **Prashant Kumar**, Munish Vashishath, Khushboo, Shireesh "Implementation of

Sub Domino AND Gate" National conference on New paradigms in Electronics, Communication and Computing, Echelon Institute of Technology, Faridabad, Haryana, May 2012.

69. **Prashant Kumar**, Munish Vashishath, "OP-AMP simulation and measurement for various parameters" Symposium on Nanotechnology: Interdisciplinary Aspects-2012, YMCAUST, Faridabad, Haryana, 2012.
70. **Prashant Kumar**, Khushboo and Nisha,"Charge Restoring and Leakage Reduction Techniques in Domino Circuit", 2nd National conference on Emerging trends in Electronics & Information Technology, AITM Palwal, Haryana, INDIA, April, 2012.
71. **Prashant Kumar**, Munish Vashishath and P K Bansal "Analytical comparison of Different 1- bit full adder scheme for 250nm CMOS technology" National conference on Science in Media (SIM 2012), YMCAUST, Haryana, 2012.
72. **Prashant Kumar**, Neeraj Gupta "Linearity based Comparison of CMOS VCOs" 1st International Conference on Advanced Computing & Communication Technology, RG Education Society, Rohtak, Haryana, INDIA, Jan, 2011.
73. **Prashant Kumar**, Pankaj Negi "Analysis & Comparison of Different 1 Bit Full adders for various Parameter" 2nd National Conference on Electronics Design & Communication Technology, HCTM, Kaithal, Haryana, 14-15 May, 2010.
74. **Prashant Kumar**, Pankaj Negi, "Analytical Comparison of Different 1-Bit full adder's scheme for 250nm CMOS Technology", International conference on Biomedical Engineering and Assistive Technology, Dr. B R Ambedkar National Institute of Technology, Jalandhar, Punjab, INDIA, December, 2010
75. Neeraj Gupta and **Prashant Kumar**, "Design Technologies for Low Voltage and Low Power VLSI", proceedings of International conference at PDM College Bahadurgarh on 16-18 June 2009.
76. Neeraj Gupta, **Prashant Kumar**, K. S. Yadav, "Comparative study of various Ring Oscillator based CMOS VCO for Linearity", proceedings of International conference at PDM College Bahadurgarh on 16-18 June 2009.
77. **Prashant Kumar** and Neeraj Gupta, "Suggestive techniques for the protection of IC from ESD" proceedings of COMMUNE 08 at NIT Kurukshetra, 2008.
78. **Prashant Kumar,** Sandeep Dhariwal and Neeraj Gupta "A Low Power Minimum Transistor CMOS XOR-XNOR Full Adder" National Conference On Recent Trends in Electronics & Communication held on 10-11 April 2008, SUSCET, Tangori, Mohali, Punjab.
79. **Prashant Kumar**, Prashant Yadav and Suman Dahiya, "Comparison and analysis of different differential amplifiers structures", ISFT Conference on Technology for Rural India: Challenges & Perspective, NSIT, New Delhi, June, 2008.
80. **Prashant Kumar**, Manoj K. Taleja, "Techniques for Frequency compensation of a Two stage operational Amplifier "National Conference on Emerging Trends in Engineering & Technology (NCETET-08), DCRUST, Murthal, Haryana, 26-27 May 2008.