



**Dr. Harish Kumar**  
B.Tech.(CE), M.Tech. (CE), Ph.D(CE)  
Associate Professor  
Department of Computer Engineering  
J. C. Bose University of Science and Technology, YMCA  
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## PERSONAL

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Name : **Dr. Harish Kumar**  
Date of Birth : 1<sup>st</sup> July, 1982  
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## PROFESSIONAL QUALIFICATIONS

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- i) **Ph.D.** in Computer Engineering from YMCA University of Science & Technology, Faridabad (October, 2017).
- ii) **M.Tech.** in Computer Engineering from YMCA Institute of Engineering, Maharishi Dayanand University, Rohtak (2006).
- iii) **B.Tech.** in Computer Engineering from YMCA Institute of Engineering, Maharishi Dayanand University, Rohtak (2004).

## EXPERIENCE

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**18 Years** of Teaching & Research experience in the field of Software Testing, Software Engineering, Software Project Management, IT Management, Computer Programming, Operating Systems.

- i) Presently working as Associate Professor (from 01.07.2020) in the Department of Computer Engineering, J. C. Bose University of Science and Technology, YMCA, Faridabad.
- ii) Worked as Assistant Professor from 03.01.2006 to 30.06.2020 in the Department of Computer Engineering, J. C. Bose University of Science and Technology, YMCA, Faridabad.

## THRUST AREAS

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- Software Testing
- Software Engineering
- Data Analytics and Machine Learning
- IT Management

## STUDENTS GUIDED FOR RESEARCH/ PROJECTS

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i) <b>M. Tech.</b>	:	30(Completed), 02 (In progress)
ii) <b>Ph.D. (Computer Engineering)</b>	:	03 (In progress)

## PUBLICATIONS:

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<b>Total Number of Publications</b>	<b>:</b>	<b>30</b>
a) Research Papers (Published) International Journals	:	16
b) Research Papers (Published) National Journals	:	04
c) Research papers in Proceedings of conferences	:	06
d) Presentations in Conferences	:	04

## LIST OF PUBLICATIONS:

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1. Harish Kumar & Naresh Chauhan, “Identifying and analysing the research challenges in Test case prioritization”, in International Journal of Computer Science & Engineering System, Vol. 6, No. 3, 2012, pp. 88-98.
2. Harish Kumar & Naresh Chauhan, “A Regression Testing Technique Using Du-Dc Paths” in YMCAUST IJR (YMCAUST International Journal of Research) Vol.3 Issue I. Jan, 2015 pp 99-104 ISSN: 2319-9377.
3. Harish Kumar & Naresh Chauhan, “A Unit – Test Case Prioritization Technique Based on Source Code Analysis” in International Journal of Advanced Research in Computer Science and Software Engineering, Volume 5 Issue 4 April 2015 ISSN: 2277 128X.
4. Harish Kumar & Naresh Chauhan, “HSTCP: A Tool for Hierarchical System Test Case Prioritization”, in International Journal of Knowledge Based Computer Systems, Vol. 3 Issue 1, 2015, pp 8-12.
5. Harish Kumar & Naresh Chauhan, “A Module Coupling Slice Based Test case Prioritization Technique”, in International Journal of Modern Education and Computer Science (IJMECS) Vol. 7, No. 7, July,2015, pp 8-16. ISSN: 2075-0161 (Print), ISSN: 2075-017X (Online).