



Department of Electronics Engineering

Ref. No.: EE/PhD/2025/ 665 4

Date: 30.01.2025

NOTICE

The Department of Electronics Engineering will schedule an **interview for admission** to the **Ph.D. Programme** on **31st January, 2025 (Friday)** at **11:00 A.M.** All the candidates are hereby instructed to report on time.

Venue: Computer Lab, Department of Electronics Engineering, J.C. Bose University of Science and Technology, YMCA, Faridabad, Haryana.

Instructions for Candidates:

1. Candidates must bring their **original documents** related to admission for verification.
2. Carry **one set of photocopies** of all relevant documents along with a copy of the admission form. Also, provide category-related documents (if applicable) and proof of domicile.
3. Bring **two recent passport-size photographs**.
4. Prepare a **15-minute presentation** along with a **two-page research write-up** as a research statement.

*Sanjay
30/1/25*
Coordinator

Recd on 25/1/25
Chairperson
Dept. of Electronics Engineering
J.C. Bose University of Science
(Electronics Engineering)
and Technology, YMCA, Faridabad

cc :

- Dean (FET)
- Academics Section (For Records)
- Admission Section (For Records)
- IT Cell with a request to upload on University Website