

J C Bose University of Science and Technology, YMCA, Faridabad

Department of Electronics Engineerig

M.Tech. (ECE/VLSI Design) Admission July 2026

Status of Vacant seats after 1st counselling on 17.07.2026

Sr. No	Category Name	ECE			VLSI		
		Total Seats	No. of Filled Seats	No of Vacant Seats	Total Seats	No. of Filled Seats	No of Vacant Seats
1	ROHC(O)	2	0	2	2	0	2
2	ROHC(F)	1	0	1	1	0	1
3	HOGC(O)	5	2	3	6	1	5
4	HOGC(F)	2	0	2	2	0	2
5	SC(O)	3	0	3	2	0	2
6	SC(F)	0	0	0	1	0	1
7	BC(O)	3	0	3	3	0	3
8	BC(F)	2	0	2	1	0	1
9	HOGC(EWS)	1	0	1	1	0	1
10	HOGC(Orphan)	1	0	1	1	0	1
11	KM	1	0	1	1	0	1
Total		21	2	19	21	1	20



Chairperson
Dept. of Electronics Engineering