



J.C. Bose University of Science & Technology, YMCA, Faridabad, Haryana
(A Haryana State Government University)
(Established by Haryana State Legislative Act No. 21 of 2009 & Recognised by UGC Act 1956 u/s 22 to Confer Degrees)
Accredited 'A+' Grade by NAAC

Ref. No. : JCBUST/EE/25/621

Date : 01.09.2025

Subject : Walk-In On -Spot Admission for M.Tech (ECE/ VLSI Design) Admission 2025-26

It is hereby informed that Walk-in On-Spot Open Counseling for vacant seats in M.Tech (ECE/VLSI Design) programs for the Academic Year 2025-26 will be held as per the following schedule :

- Date : Every Working Day till 30.09.2025
- Time : 10:00 A.M. – 03:00 P.M.
- Venue : Department of Electronics Engineering, J.C. Bose University of Science and Technology, YMCA, Faridabad, Haryana

Vacant Seat Status :

1. M.Tech : Electronics and Communication Engineering

Category	Vacant Seat	
AIC	18	
Supernumerary Seat	EWS	01
	Orphan	01
	KM	01

2. M.Tech – VLSI Design

Category	Vacant Seat	
AIC	13	
Supernumerary Seat	EWS	01
	Orphan	01
	KM	01

However, the number of vacancies may increase subject to any subsequent withdrawals or cancellations. Admissions will be offered on the spot based on merit and subject to availability of seats. Candidates must report in person with all the required documents and the prescribed fee.

For more details, please visit the University website : www.jcboseust.ac.in

Washirbhat

Chairperson
26-IX-2025

Deptt. of Electronics Engineering
J.C. Bose University of Science
& Technology, YMCA, Faridabad

- System Analyst – with a request to kindly upload the notice on the University Website
- cc:
- Chairperson(Admission Committee)
- Director (Admission)
- Dy. Registrar (Academics)