



**J.C. Bose University of Science & Technology, YMCA, Faridabad, Haryana**  
(A Haryana State Government University)  
(Established by Haryana State Legislative Act No. 21 of 2009 & Recognised by UGC Act 1956 u/s 22 to Confer Degrees)  
Accredited 'A+' Grade by NAAC

### **Department of Electronics Engineering**

**Date : 11.11.2025**

#### **Ph.D ADMISSION NOTICE**

**Ref. No. JCBUST/EE/2025/773**

Ph.D interview and admission in the Department of Electronics Engineering will be held on 13<sup>th</sup> November 2025 as per the given schedule. All the candidates eligible for Ph.D. admission (exempted from entrance and eligible by entrance) should report for the admission on 13.11.2025 at 09:30 A.M. in the Conference Hall of the Department of Electronics Engineering.

<b>Date and Time of Reporting</b>	<b>Venue for Reporting</b>
<b>13.11.2025 (Thursday) at 09:30 A.M.</b>	<b>Conference Hall, Department of Electronics Engineering</b>

#### **INSTRUCTIONS:**

1. Candidates are required to report their respective department for the interview as per the date & time notified by the respective department.
2. **Candidates are required to bring a brief research proposal (3–5 pages) along with a presentation at the time of the interview.**
3. List of documents required at the time of interview is given below.

#### **LIST OF DOCUMENTS REQUIRED AT THE TIME OF PHYSICAL REPORTING IN THE UNIVERSITY**

- i. Printout of the downloaded application form filled on the University Samarth portal
- ii. Matriculation/High School Certificates
- iii. Undergraduate Examination certificate/DMCs
- iv. Post Graduate Examination/Qualifying Examination certificate/DMCs
- v. Character Certificate from the Head of Institution last attended (Annexure-I)
- vi. Valid Certificate for SC/ST/BC/PH/ESM/FF/EWS (format as per respective Annexures attached at the end of the information brochure) or any other reservation certificate, if applicable, prescribed by the Government
- vii. Four passport-size-coloured photographs
- viii. Medical Fitness Certificate from a Gazetted Medical Officer (format as per Annexure-XIII)

- ix. Haryana resident domicile certificate (format as per Annexure-II) (if applicable) (Candidates who have passed their qualifying examination from a recognized institution located in the State of Haryana will be deemed to be Haryana residents and will not be required to submit certificates of bonafide residents of Haryana.)
- x. Proof of annual parental income in the form of an income certificate issued by the authority prescribed for issue of resident/income certificates as specified in instruction No.22/132/2013-I-HR3, dated 31.08.2022. Instructions given in memo No. 504 dated 17.12.2021 will be applicable for specifying the criteria for exclusion of persons with backward classes as creamy layer.
- xi. Migration certificate in original (wherever applicable)
- xii. Anti-ragging affidavit (Notary attested) by the candidate and parents/guardian of the candidate as per format of Annexure-XI & Annexure-XII respectively.
- xiii. Valid and qualified entrance exam exemption proof, if applicable (GATE/NET/JRF/M.Phil.)
- xiv. For Ph.D in Part-time mode and Ph.D for industry-sponsored, refer Annexure-II of Ph.D Information Brochure for additional documents.

For various Annexures refer to Information Brochure (UG/PG) 2025-26 of the University. Link is given below:

[https://jcboseust.ac.in/assets/files/Information-Brochure-2025-2026-JCBoseUST\\_03-07-2025.pdf](https://jcboseust.ac.in/assets/files/Information-Brochure-2025-2026-JCBoseUST_03-07-2025.pdf)

**List of eligible Candidates for Ph.D. admission in Electronics Engineering**

**(exempted from entrance & eligible by entrance)**

Sr. no.	Application ID	Name	Father's Name	Category	Remarks
1	JCBOSE0008638	Rohit Arora	Anil Kumar	Haryana Open General Category (HOGC)	NET Ph.D. Qualified
2	JCBOSE0009182	Madhu Nimesh	Narender Kumar Nimesh	Scheduled Caste (SC)	NET Ph.D. Qualified
3	JCBOSE0008836	Renu Goyal	Rajkumar	Haryana Open General Category (HOGC)	Entrance Exam Qualified

**Note : No individual mail/communication will be made regarding admission. Refer to University website for further updates.**

*Ice Lam  
Chairperson (11/11/2025)  
Chairperson  
Dip. in Electronics Engineering  
(Electronics Engineering)  
J.C. Bose University of Science  
and Technology, YMCA, Faridabad*