



J.C. Bose University of Science & Technology, YMCA, Faridabad, Haryana
(A Haryana State Government University)
(Established by Haryana State Legislative Act No. 21 of 2009 & Recognised by UGC Act 1956 u/s 22 to Confer Degrees)
Accredited 'A+' Grade by NAAC

Department of Electronics Engineering

Date : 28.07.2025

Notice

Schedule of Physical Counseling for Admission in M.Tech (ECE / VLSI Design) for the Academic Session 2025-26

It is hereby notified that the Department of Electronics Engineering shall be conducting Physical Counseling (3rd and 4th Counseling) for admission in M.Tech (ECE/VLSI Design) for the Academic Session 2025-26 on 30.07.2025 for the left over seats, as per details given below :

3rd Counseling :

Reporting Date : 30.07.2025

Time : 09:00 A.M.

Vacant Seat Status : List attached as per Annexure – A

Venue : Control System Lab, Department of Electronics Engineering

4th Counseling :

Reporting Date : 30.07.2025

Time : 12:30 P.M.

Venue : Control System Lab, Department of Electronics Engineering

*** Note :** All the left over seats after 3rd Counseling shall be converted into AIC (All India Category) without reservation as per merit.

Chairperson

(Electronics Engineering)

Deptt. of Electronics Engineering
J.C. Bose University of Science
and Technology, YMCA, Faridabad

cc :

1. Director (Admissions)
2. Academics Branch

Annexure - A



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Department of Electronics Engineering

Date : 28.07.2025

Vacant Seat Status after Second Counseling of M.Tech (Electronics and Communication Engineering) for Admission in session 2025-26 [Held on 25.07.2025]

Category	Vacant Seat	
AIC/HOGC	10	
SC	03	
BC	04	
Supernumerary Seat	EWS	01
	Orphan	01
	KM	01

Vacant Seat Status after Second Counseling of M.Tech (VLSI Design) for Admission in Session 2025-26 [Held on 25.07.2025]

Category	Vacant Seat	
AIC/HOGC	09	
SC	03	
BC	04	
Supernumerary Seat	EWS	01
	Orphan	01
	KM	01

Note :

1. Interested candidates are required to report according to the vacant seat matrix mentioned above at 09:00A.M on 30.07.2025
2. Seat may increase in any category as per the seat availability at the time of counseling.
3. All the left over seats will be converted to AIC without any reservation in 4th Counseling on 30.07.2025 at 12:30 P.M.

Wardhata
28-VII-2024
Chairperson
Deptt. of Electronics Engineering
J.C. Bose University of Science
and Technology, YMCA, Faridabad

Department of Electronics Engineering
Tentative Merit List for Admission (2025-26) in M.Tech. ECE/VLSI
3rd and 4th Counseling Counseling Date : 30.07.2025

Date:- 28/07/2025

Sr. No.	Application No.	Name	Father's Name	Gender	Category Applied	Category Selected	GATE Qualified (Yes/No)	GATE Score (If Qualified)	8 th Semester Percentage/CGPA	7 th Semester Percentage/CGPA	Remarks
1	25REG00008785	Karan Verma	Sanjeev Verma	Male	General	HOGC(O)	No	NA	85.82		
2	25REG00007430	Rahul	Ram Kumar	Male	General	ROHC(O)	No	NA	66.43		
3	25REG00000118	Gaurav	Ravinder Kumar	Male	BC(A)	BC-A(O)	No	NA	91.00%		Provisionally Admitted
4	25REG00002564	Vibhanshu	Anil Sharma	Male	General	HOGC(O)	No	NA	77.13%		
5	25REG00000158	Sumit Kumar Mangain	Pawan Kumar Mangain	Male	General	HOGC(O)	No	NA	77.00%		
6	25REG00000158	Sumit Kumar Mangain	Pawan Kumar Mangain	Male	General	HOGC(O)	No	NA	77.00%		
7	25REG00008639	Preeti	LalaRam	Female	SC	SC (F)	No	NA	70.12%		Provisionally Admitted
8	25REG00001269	Manikant Gupta	Raj Kumar Gupta	Male	General	HOGC(O)	No	NA	68.84%		
9	25REG00001269	Manikant Gupta	Raj Kumar Gupta	Male	General	HOGC(O)	No	NA	68.84%		
10	25REG00008882	Kalpna Kalyan	Rajesh Kumar Kalyan	Female	General	HOGC(F)	No	No	NA	75.40%	Provisionally Admitted

Below applicants has not mentioned/attached qualifying degree proof, so these are not assigned any merit. Still they can appear for counselling

Sr. No.	Application No.	Name	Father's Name	Gender	Category Applied	Category Selected	GATE Qualified (Yes/No)	GATE Score (If Qualified)	8 th Semester Percentage/CGPA	7 th Semester Percentage/CGPA	Remarks
	25REG00007634	Harsh Mehra	Surender Kumar	Male	SC	SC(O)	No	NA	NA	NA	
	25REG00001331	Vishakha Chhoker	Ravinder Kumar	Female	General	HOGC(F)	No	NA	NA	NA	
	25REG00003931	Cheshank	Mahender Singh	Male	General	HOGC(O)	No	NA	NA	NA	
	25REG00002916	Dolly rajput	Shriram	Female	SC	ROHC (F)	Has completed B.Sc. only (55.40%). M.Sc. Proof required otherwise not eligible				

This merit list is purely tentative and same will be finalised after verification of the documents


 Chairperson 28-07-2025
 Electronics Engineering
 Deptt. of Electronics Engineering
 Science
 Address:

